

Experienced IC Layout Engineer for Microelectronics Integration Design Center - A OT (TM)

あなたの仕事内容

Responsibilities

Generation of block and Chip layouts based on analog circuits/schematics. The layout engineer chooses appropriate methods to reach robust DRC compliant layout with efficient area utilization. Layout engineer works very closely with the analog circuit designer.

Detailed tasks description

- Creating and integrating analog layouts on block and top level
- Floorplanning, pin placement, parasitic extraction
- Support design engineer to reach Speed, area, power, DFM & ESD/EMV requirements
- DRC,LVS & Tape out support

あなたのプロフィール

- Bachelor degree in electronics engineering
- 5-10 years of layout design experience
- Advanced knowledge in area of analog circuit layout
- Experienced in doing layouts in BCD technology (Technologies: 65 - 180nm)
- Very good knowledge in the area of deep sub micron effects (Parastics, coupling, matching etc.)
- Advanced understanding of layout tools (design, DRC, LVS, Extraction)
- Basic knowledge of analog circuit design

Languages

- English

Capabilities

- Must be a team player with good communication and negotiation skills
- Customer focus (anticipates and understands the customers' needs)
- Motivation and inspiration (operates with integrity and demonstrates dynamism)
- Taking initiative

オファー



ジョブID

REF84978A

業務分野
研究開発

勤務地

Timișoara

リーダーシップレベル

Leading Self

勤務に関する柔軟性

Hybrid Job

法的事項

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SRL**

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