## **Ontinental**

# Principal / Senior Engineer, Product Testing, Technical Operations Management

### 담당 업무

The Principal / Senior Engineer, Product Testing is responsible for IC component testing. This role demands a high level of expertise and commitment to ensuring the quality and reliability of our products.

- Strategically define, procure, and construct test infrastructure essential for IC component development, testing, and qualification in accordance with stringent Design and Quality requirements.
- Conduct comprehensive analyses of various test systems and solutions, evaluating cost-effectiveness, maintenance requirements, availability, and associated risks.
- Alignment and optimization of test infrastructure across multiple R&D development centers, external suppliers, and OSAT companies to ensure cohesive operations.
- Establish criteria and execute 'Make/Buy' decisions regarding the establishment of internal Test Environments versus the procurement of tests from external Test Houses.
- Analyze planned Test Strategies and determine the requisite Test Infrastructure to support organizational objectives.
- Implement robust monitoring and optimization protocols for test infrastructure usage to guarantee efficient and cost-effective availability.
- Compile and present comprehensive reports on test infrastructure status, performance metrics, and critical milestones to senior management.
- Conduct thorough evaluations of external Partners' capabilities, assessing their expertise, quality standards, and agility in meeting organizational needs.
- Systematically plan, maintain, and assess the qualification of Test Infrastructure in strict adherence to industry norms and standards.
- Document the capabilities, design specifications, and integration processes of Test Infrastructure.
- Engage in strategic collaboration with cross-functional teams to ensure alignment of test infrastructure with product development roadmaps.

#### 지원자 프로필

- Degree in Electronics/Electrical Engineering (or relevant) with minimum five years' working experience in semiconductor (e.g., process or product), or subcon management (e.g., Foundries or OSAT).
- Good knowledge in semiconductor manufacturing processes (e.g., wafer process technologies and package assembly), electronic testing (e.g., DFT, Test Hardware, Test Development) and automotive electronic qualification requirements (e.g., AEC-Q100/101).



직무-아이디 **REF84276X** 

모집 분야 공학

지사 Singapore

리더십 레벨 Leading Self

근무 유형 Onsite Job

법률 고지 Continental Automotive Singapore Pte. Ltd.

- Preferable with strong technical proficiency in DFT, Test development and test programming.
- Good interpersonal and communication skills (e.g., Business English, Presentations) to interact with internal and external stakeholders.
- Strong problem-solving skills, results oriented and a quality-focused mindset.
- Team player with proactive attitude, able to work independently and travel frequently.

#### 처우 조건

Ready to drive with Continental? Take the first step and fill in the online application.

#### 기업 소개

Continental develops pioneering technologies and services for sustainable and connected mobility of people and their goods. Founded in 1871, the technology company offers safe, efficient, intelligent and affordable solutions for vehicles, machines, traffic and transportation. In 2024, Continental generated sales of €39.7 billion and currently employs around 190,000 people in 55 countries and markets.

The Automotive group sector comprises technologies for passive safety, brake, chassis, motion and motion control systems. Innovative solutions for assisted and automated driving, display and operating technologies, as well as audio and camera solutions for the vehicle interior, are also part of the portfolio, as is intelligent information and communication technology for the mobility services of fleet operators and commercial vehicle manufacturers. Comprehensive activities relating to connectivity technologies, vehicle electronics and high-performance computers round off the range of products and services.